(FILE 'USPAT' ENTERED AT 09:04:25 ON 28 AUG 1998)
L1 7 SEA FLOATING GATE (P) (SIC OR SILICON CARBIDE)

FILE USPAT

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*                                                               *
*                  W E L C O M E   T O   T H E                  *
*          U. S.   P A T E N T   T E X T   F I L E              *
*                                                               *
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=> d 1-7

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4. 5,629,222, May 13, 1997, Method of forming semiconductor memory device by selectively forming an insulating film on the drain region; Shunpei Yamazaki, et al., 438/259, 261, 264 [IMAGE AVAILABLE]

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6. 4,769,686, Sep. 6, 1988, Semiconductor device; Masatada Horiuchi, et al., 257/373, 384, 404 [IMAGE AVAILABLE]

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1. 5,293,212, Mar. 8, 1994, Non-volatile semiconductor memory device allowing erase of storage data of an arbitrary memory cell and method of erasing data in non-volatile semiconductor memory device; Makoto Yamamoto, et al., 365/218, 185.13, 185.18, 185.26, 185.29, 185.33 [IMAGE AVAILABLE]

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     (FILE 'USPAT' ENTERED AT 08:33:55 ON 28 AUG 1998)
L1          242 SEA FLOATING GATE AND CONTROL GATE AND DRAM
L2            1 SEA L1 AND BARRIER ENERGY


     FILE USPAT
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       *                                                             *
       *              W E L C O M E   T O   T H E                    *
       *          U. S.   P A T E N T   T E X T   F I L E            *
       *                                                             *
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